

Notice of References Cited

Application/Control No.

10/602,570

Applicant(s)/Patent Under
Reexamination
GRINCHUK ET AL.

Examiner

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Art Unit

2825

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U.S. PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)

U Cortadella, "Timing-Driven Logic Bi-Decomposition", June 2003, IEEE Transactions on Computer-Aided Design of Integrated Circuits, vol. 22, iss. 6, pp. 675 - 685

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